

Docket No. 251383US2CONT
IN RE APPLICATION OF: Takashi YAMADA, et al.
SERIAL NO: 10/828,337
FILED: April 21, 2004
FOR: SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS SEPARATES SAME WITH PLURAL ISOLATION REGIONS



IFW

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:
Transmitted herewith is an amendment in the above-identified application.

No additional fee is required
 Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
 Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	17	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	2	MINUS	3	0	x \$200 =	\$0.00
APPLICATION SIZE		MINUS	100	0 (each addtl. 50 sheets)	x \$250 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS				+ \$360 =		\$0.00
TOTAL OF ABOVE CALCULATIONS						\$0.00
<input type="checkbox"/> Reduction by 50% for filing by Small Entity						\$0.00
TOTAL						\$0.00

A check in the amount of \$0.00 is attached.
 Credit card payment form is attached to cover the fees in the amount of \$0.00
 Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
 If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters
Registration No. 28,870

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)



DOCKET NO: 251383US2CONT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TAKASHI YAMADA, ET AL. : EXAMINER: SOWARD, I. M.

SERIAL NO: 10/828,337 :

FILED: APRIL 21, 2004 : GROUP ART UNIT: 2822

FOR: SEMICONDUCTOR CHIP WHICH
COMBINES BULK AND SOI REGIONS
AND SEPARATES SAME WITH PLURAL
ISOLATION REGIONS

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated February 28, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.